

# MOHD HAIDAR HAMZAH

Design Automation Engineer



## About Me

With over 15 years as a Technical Staff Member at Intel, I bring extensive experience in machine learning development for reliability verification in the Analog domain and in Process Design Kit (PDK) development for CAD tools. My expertise includes RF modeling and proficiency in Python, Perl, and TCL, alongside skills in HTML, Java, and Dart (Flutter).



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## PROGRAMMING LANGUAGE

- Python
- Perl
- Tcl
- Skill Cadence
- Java
- HTML
- Dart Flutter

## EXPERTISE

- Machine Learning
- Creativity
- Digital Marketing
- Negotiation
- Critical Thinking
- Leadership

## EXPERIENCE

### Member of Technical Staff

#### Design Automation Engineer

##### Intel

2015 - Now

Involved with Analog and Digital design and automation for design flow. The Analog & Digital IC Design Engineer will be part of the Intel SOC team that is designing the system on-chip products.

#### Physical Design Engineer

##### Altera

2010 - 2015

Responsible for VLSI component layout and data entry, translate schematic into layout geometry, owning Cluster level and up to full chip layout integration and verification. Expertise in full custom layout analog, mixed signal and IO cells

#### RF Modelling Engineer & Cad Engineer

##### Silterra

2010 - 2015

Develop/maintain software used for semiconductor device modeling, including full lifecycle application development; semiconductor device dc, small signal and RF modeling; device and circuits electrical parameters characterization

#### PV Engineer

##### Intel

2007 - 2010

Primarily responsible for SDC generation, synthesis, static timing analysis and power analysis of digital logic. The engineer also needs to be proficient in die-area optimized and low-power RTL design and RV implementation flows.

## EDUCATION

### Universiti Teknologi Malaysia

Master of Engineering (Electrical-Computer and Microelectronic System)

2008-2010

### Universiti Teknologi MARA

B.Eng (Hons) in Electrical Engineering (Electronic)

2005-2007

## SKILLS SUMMARY

Design Process

90%

Project Management

90%